

Time Table
VII & VIII Semester B. Tech. (ECE)

	8-9 AM	9-10 AM	10-11 AM	11-12 PM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM	5-6 PM
Wednesday, 17 February, 2021								UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit	
Thursday, 18 February, 2021								UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC1A – VLSI Lab : MY, Lokesh, Basudha, Amit	
Friday, 19 February, 2021								UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit	
Saturday, 20 February, 2021								UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit	
Monday, 22 February, 2021								UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit	
Tuesday, 23 February, 2021								UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit	
Wednesday, 24 February, 2021								UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC3B – VLSI Lab :	
Thursday, 25 February, 2021								UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit	

Friday, 26 February, 2021								UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit
Saturday, 27 February, 2021								UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC1A – VLSI Lab :
Monday, 1 March, 2021								UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit
Tuesday, 2 March, 2021								UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit
Wednesday, 3 March, 2021								UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit
Thursday, 4 March, 2021								UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit
Friday, 5 March, 2021								UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC3B – VLSI Lab : MY, Lokesh, Basudha, Amit
Saturday, 6 March, 2021								UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit

Monday, 8 March, 2021	UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
	UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC1A – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit
	Btech Project Presentations	Btech Project Presentations	Btech Project Presentations
Tuesday, 9 March, 2021	UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
	UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit
	Btech Project Presentations	Btech Project Presentations	Btech Project Presentations
Wednesday, 10 March, 2021	UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
	UG Sys. Des. Lab – II : EC3B – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit
	Btech Project Presentations	Btech Project Presentations	Btech Project Presentations
Thursday, 11 March, 2021	UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
	UG Sys. Des. Lab – II : EC1A – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit
	Btech Project Presentations	Btech Project Presentations	Btech Project Presentations
Friday, 12 March, 2021	UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
	UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit	UG Sys. Des. Lab – II : EC3B – VLSI Lab : MY, Lokesh, Basudha, Amit
	Btech Project Presentations	Btech Project Presentations	Btech Project Presentations
Saturday, 20 March, 2021			UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
			UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit
Monday, 22 March, 2021			UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
			UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit
Monday, 22 March, 2021			UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
			UG Sys. Des. Lab – II : EC1A – VLSI Lab : MY, Lokesh, Basudha, Amit
Monday, 22 March, 2021			UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
			UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit

Tuesday, 23 March, 2021								
Wednesday, 24 March, 2021							UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit
Thursday, 25 March, 2021							UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit
Friday, 26 March, 2021							UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit
Saturday, 27 March, 2021							UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC3B – VLSI Lab : MY, Lokesh, Basudha, Amit
Tuesday, 30 March, 2021							UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit
Wednesday, 31 March, 2021							UG Sys. Des. Lab – I : EC4A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC4B – VLSI Lab : MY, Lokesh, Basudha, Amit
Thursday, 1 April, 2021							UG Sys. Des. Lab – I : EC4B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC1A – VLSI Lab : MY, Lokesh, Basudha, Amit
Saturday, 3 April, 2021							UG Sys. Des. Lab – I : EC1A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC1B – VLSI Lab : MY, Lokesh, Basudha, Amit
Monday, 5 April, 2021							UG Sys. Des. Lab – I : EC1B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti	UG Sys. Des. Lab – II : EC2A – VLSI Lab : MY, Lokesh, Basudha, Amit

Tuesday, 6 April, 2021								UG Sys. Des. Lab – I : EC2A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC2B – VLSI Lab : MY, Lokesh, Basudha, Amit
Wednesday, 7 April, 2021								UG Sys. Des. Lab – I : EC2B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC3A – VLSI Lab : MY, Lokesh, Basudha, Amit
Thursday, 8 April, 2021								UG Sys. Des. Lab – I : EC3A – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC3B – VLSI Lab : MY, Lokesh, Basudha, Amit
Friday, 9 April, 2021								UG Sys. Des. Lab – I : EC3B – EMBD Lab : Rashi, Giridhar, Ritu, Trapti
								UG Sys. Des. Lab – II : EC4A – VLSI Lab : MY, Lokesh, Basudha, Amit